2022 International Conference on Analog VLSI Circuits (AVIC 2022) Hiroshima, Japan October 31st – November 2nd, 2022

General Co-chairs

Satoru Shingai, Canon Inc. Toshihiko Hamasaki, Hiroshima Institute of Technology

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Local Arrangement Co-chairs

Yoshihiro Masui, Hiroshima Institute of Technology Tomochika Harada, Yamagata University

Financial Chair Soichiro Ohyama, SiTime Japan

Important Dates

Paper Submission Deadline: June 30, 2022 Author Notification Due: August 8, 2022 **Registration Starts:** September 1, 2022 Final Paper Submission Deadline: **September 10, 2022**

Conference:

October 31– November 2, 2022

Contact:

avic2022@ieej-ect.org https://www.avic2022.org/

Selected papers will be published in a SPRINGER journal special issue

Organizers





Hiroshima Institute of Technology

Scope

The Research Committee on Electronic Circuits of the Institute of Electrical Engineers of Japan (IEEJ) in cooperation with Department of Information and Communication Engineering, Hiroshima Institute of Technology will hold 2022 International Conference on Analog VLSI Circuits (AVIC) in Hiroshima, Japan on October 31st –November 2nd 2022.

AVIC is the successor of International Analog VLSI Workshop, which had been held for 25 years. The purpose of this conference is to discuss state-of-the-art ideas and results from researches on analog VLSI circuits and their applications. It has contributed to long-standing worldwide community of analog VLSI circuit experts in industry and academia.

Topics

AVIC 2022 covers topics related to analog and mixed-signal integrated circuits including but not limited to:

Analog and Mixed-signal Integrated Circuits:

Amplifiers, Filters, Comparators, Oscillators, Multipliers, PLLs, RF ICs, Voltage/Current References, Sample-And-Hold Circuits, A/D and D/A Converters, High Speed IO Interface, Energy Harvesting, Power Management

Analog VLSI Design Automation:

Layout, Simulation, HDL, Analog IP

- Signal/Information Processing and Applications: Communication, Multimedia, Automotive Electronics, **Biomedical Electronics, Consumer Electronics, Non**linear Signal Processing, Neural Network, Sensing and Sensor Networks, Space Electronics
- Digital Circuits and Digital Signal Processing

Paper Submission

Paper submission is accepted via the official website https://www.avic2022.org/ Authors are required to submit a full paper in English, in IEEE format, along with information including paper title, author name(s), affiliation(s) of author(s), and the corresponding author (name, postal and e-mail addresses). Notification will be sent to the authors via e-mail. Accepted papers must be resubmitted as the final (cameraready) ones. At least one of the authors of a paper must register and give a presentation at the conference.

Visit AVIC 2022 website for additional information:

https://www.avic2022.org/

Conference Venue

Hiroshima Institute of Technology 2-1-1 Miyake, Saeki-ku, Hiroshima, 731-5193 Japan https://www.it-hiroshima.ac.jp/



