Paper Title

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**Abstract** Most word limits are around 200 words.

**Keywords:** AVIC, ECT, Integrated circuit, Analog design, ... , (5–6 words).

1. **Introduction**

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1. **Section 2**

**2.1 Subsection**

1. **Conclusion**

References

1. S.M. Park and H.-J. Yoo, “1.25-Gb/s regulated cascode CMOS transimpedance amplifier for gigabit ethernet applications,” IEEE J. Solid-state Circuit vol. 39, no. 1, pp. 112–121, Jan. 2004.



Fig. 1. AVIC logo.

Table 1. Device parameters.

|  |  |
| --- | --- |
| M1 [µm] | W/L= 10/ 0.18 |
| M2 [µm] | W/L= 60/ 0.18 |
| C |  |